

#### Applications

- IEEE802.11b DSSS WLAN
- IEEE802.11g,n OFDM WLAN
- Embedded, SiP modules

## Features

- Dual Mode IEEE802.11b & IEEE802.11g
- Integrated PA, digital bias control, 50Ω input and output match, 3.2GHz TX Filter.
- Integrated harmonic filter.
- Integrated load insensitive Power Detector, with <1dB error at 2:1 mismatch</li>
- 21 dBm Output Power, 802.11b, 11 Mbps
- 18.5dBm @ 3.0 % EVM, 802.11g, 54 Mbps
- 2.3 V to 4.8 V direct to battery supply
- Lead free, Halogen free, ROHS compliant, 2 x2x0.9 mm QFN package, MSL 1

## **Ordering Information**

Part No.	Package	Remark
SE2574BL-R	8 pin QFN	Samples
SE2574BL-R	8 pin QFN	Tape and Reel
SE2574BL-R- EK1	N/A	Evaluation kit

## **Functional Block Diagram**

#### EN VCC VCC2 FF IN Filter Fi

#### Figure 1: Functional Block Diagram

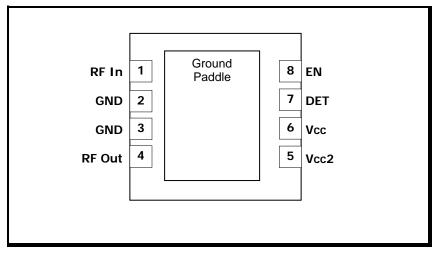
# Product Description

The SE2574BL-R is a complete 802.11 b/g WLAN discrete power amplifier. The device provides all the functionality of the power amplifier, power detector, filter, associated input, inter-stage and output matching in an ultra compact 2mm x 2mm x 0.9mm form factor.

The SE2574BL-R is designed for ease of use, with all the critical input and output matching integrated. The SE2574BL-R includes a transmitter power detector with 20 dB of dynamic range and a digital Enable for power on/off control. Harmonic filters and an input 3.2GHz LO rejection filter are integrated on-chip. The power ramp rise/fall time is 0.7 µs typical.



## Pin Out Diagram





## **Pin Out Description**

Pin No.	Name	Description	
1	RF In	RF Input (No DC voltage on the pin, but DC short to ground)	
2	GND	Ground	
3	GND	Ground	
4	RF Out	RF Output (No DC voltage on the pin, DC open to ground)	
5	VCC2	Final Stage Supply Voltage (May attach directly to battery)	
6	VCC	First Stage Supply Voltage (May attach directly to battery)	
7	DET	Power Detector Output	
8	EN	Power Amplifier Enable	
Die paddle	GND	Ground	



## **Absolute Maximum Ratings**

These are stress ratings only. Exposure to stresses beyond these maximum ratings may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Definition	Min.	Max.	Unit
VCC	Supply Voltage on VCC	-0.3	5.5	V
EN	DC input on EN	-0.3	4.0	V
ТΧ	RF Input Power. ANT terminated in $50\Omega$ match	-	12.0	dBm
TA	Operating Temperature Range	-40	85	°C
Тѕтс	Storage Temperature Range	-40	150	°C
ESD <sub>HBM</sub>	JEDEC JESD22-A114, all pins		1000	V

## **Recommended Operating Conditions**

Symbol	Parameter	Min.	Тур.	Max.	Unit
TA	Ambient temperature	-40	25	85	°C
	Supply voltage, nominal operation	2.7	3.3	5.0	
VCC	Supply voltage, output power reduced by 2dB typ	2.3	2.7		V

#### **DC Electrical Characteristics**

Conditions: VCC = 3.3V, EN = 3.3V, T<sub>A</sub> = 25 °C, as measured on Skyworks SE2574BL-R-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
lcc-g	Total Supply Current	54 Mbps OFDM signal, 64QAM 18dBm, VCC = 3.3V	-	140	-	mA
Ісс-в	Total Supply Current	11 Mbps CCK signal, BT = 0.45 20dBm, VCC = 3.3V	-	165	-	mA
Ιcq	Total Supply Current	No RF VCC = 3.3V	-	110	-	mA
ICC_OFF	Total Supply Current	EN = 0 V, No RF Applied	-	1	10	μA



## **Logic Characteristics**

Conditions: VCC = 3.3V, EN = 3.3V, T<sub>A</sub> = 25 °C, as measured on Skyworks SE2574BL-R-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Venh	Logic High Voltage (Module On)	-	1.8	-	3.6	V
Venl	Logic Low Voltage (Module Off)	-	0	-	0.4	V
Ienh	Input Current Logic High Voltage	-	-	160	180	μA
Ienl	Input Current Logic Low Voltage	-	-	20	25	μA

## **AC Electrical Characteristics**

## 802.11g/n Transmit Characteristics

Conditions: VCC = 3.3V, EN = 3.3V, T<sub>A</sub> = 25 °C, as measured on Skyworks SE2574BL-R-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Con	dition	Min.	Тур.	Max.	Unit
Fin	Frequency Range		-	2400	-	2500	MHz
Dout	Output Dowor 2.2V	54 Mbps OFDM ,	64 QAM, EVM=3%	-	18.5	-	dDm
Pout	Output Power, 3.3V	11Mbps, CCK, BT	=0.45, Mask		21		dBm
P <sub>1dB</sub>	P1dB	VCC = 3.3V		-	25.0	-	dBm
<b>S</b> 21	Small Signal Gain	-		25	27	29	dB
10.0	Small Signal Gain	Gain variation ove channel	r single 20MHz	-	0.5	-	JD
Δ <b>S</b> 21	Variation	Gain Variation over	er band	-	-	1.1	dB
2f			20dBm, 3.3V	-	-40	-35	dBm/MHz
Зf	Harmonics	1 Mbps, BPSK,	20dBm, 3.3V	-	-40	-35	dBm/MHz
tdr, tdf	Delay & rise/fall Time		50 % of V <sub>EN</sub> edge and 90/10 % of final output power level		0.7	-	μs
S11	Input Return Loss	-		10	15	-	dB
STAB	Stability	CW, Pout = 20 dBm, VCC = 3.3V 0.1 GHz – 20 GHz Load VSWR = 10:1		All non-ha than -42		•	utputs less
RU	Ruggedness	P <sub>IN</sub> = 12dBm, VC0 Load VSWR = 10:		No perma	inent dam	lage	



## **Power Detector Characteristics**

Conditions: VCC = 3.3V, EN = 3.3V, T<sub>A</sub> = 25 °C, as measured on Skyworks SE2574BL-R-EK1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	VCC = 3.3V		Unit	
			Min.	Тур.	Max.	
Fout	Frequency Range	-	2400	-	2500	MHz
PDR	Power detect range, CW	Measured at ANT	0	-	23	dBm
PDZsrc	DC source impedance on PD_OUT	-	-	1	-	kΩ
PDVNORF	Output Voltage, Po∪⊤ = No RF	Measured into $1M\Omega$	-	0.10	-	V
PDV <sub>p16</sub>	Output Voltage, Pou⊤ = 16 dBm CW	Measured into $1M\Omega$	-	0.50	-	V
PDV <sub>p19</sub>	Output Voltage, Pou⊤ = 19 dBm CW	Measured into $1M\Omega$	-	0.70	-	V
PDV <sub>p22</sub>	Output Voltage, Pou⊤ = 22 dBm CW	Measured into $1M\Omega$	-	1.0	-	V
LPF-3dB	Power detect low pass filter -3dB corner frequency	Measured into $1M\Omega$		2.5		MHz

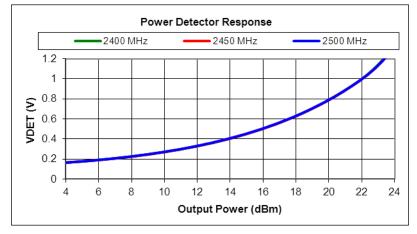


Figure 3: SE2574BL-R Power Detector Characteristics



## Package Diagram

This package is Pb free and RoHS compliant. The product is rated MSL1.

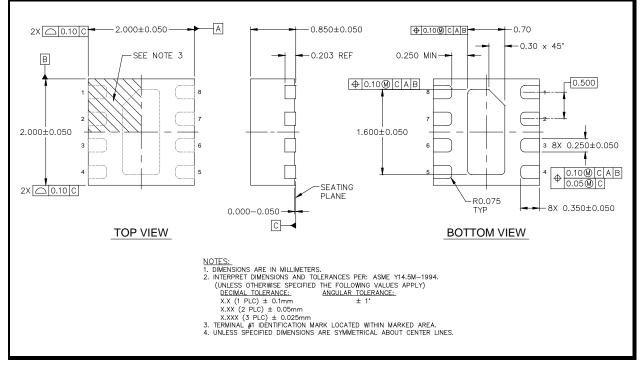


Figure 4: SE2574BL-R Package Diagram



## **Recommended Land Pattern**

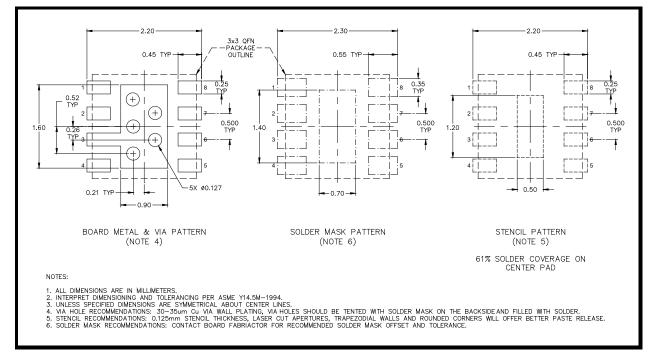


Figure 5: SE2574BL-R Package Diagram

#### **Branding Information**

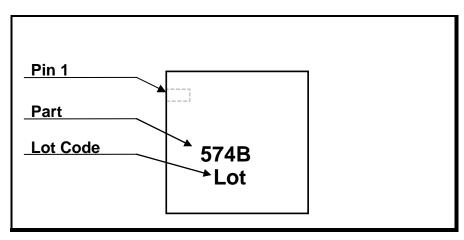


Figure 6: SE2574BL-R Branding and Pin 1 Location (Top View)



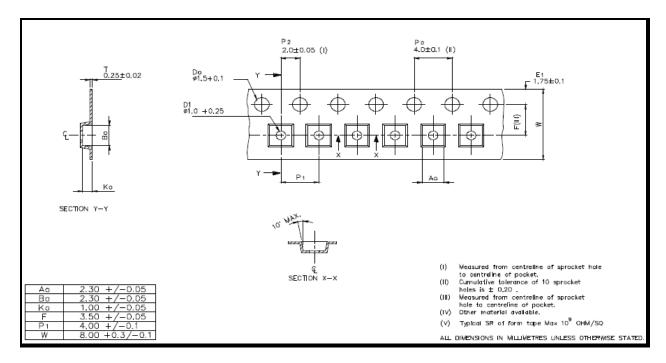
#### Package Handling Information

Because of its sensitivity to moisture absorption, instructions on the shipping container label must be followed regarding exposure to moisture after the container seal is broken, otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly. The SE2574BL-R is capable of withstanding a Pb free solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is manually attached, precaution should be taken to insure that the device is not subjected to temperatures above its rated peak temperature for an extended period of time. For details on both attachment techniques, precautions, and handling procedures recommended by Skyworks, please refer to:

- Skyworks Application Note: "QFN solder reflow and rework information application note", Document Number QAD-00045
- Skyworks Application Note: "Handling, packing, shipping and use of moisture sensitive QFN application note", Document Number QAD-00044

#### **Tape and Reel Information**

Parameter	Value
Devices Per Reel	3000
Reel Diameter	7 inches
Tape Width	8 millimeters







## **Document Change History**

Revision	Date	Notes	
1.0	February-10-2011	Created	
1.1	February-24-2011	Updated POD	
1.2	April-28-2011	Updated Part marking and updated specification	
1.3	December-16-2011	Updated specifications	
1.4	February-27-2012	Updated Tape and reel information.	

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